

09/654776

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,724,036 B1  
DATED : April 20, 2004  
INVENTOR(S) : Chia-Ta Hsieh et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [54], Title, please add the word -- **FLOATING** -- so that it reads:

**"STACKED-GATE FLASH MEMORY CELL WITH FOLDING FLOATING  
GATE AND INCREASED COUPLING RATIO".**

Signed and Sealed this

Twenty-second Day of February, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*